

WEST Search History

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DATE: Friday, December 10, 2004

Hide? Set Name Query

Hit Count

DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

<input type="checkbox"/>	L29	applied with polymer with film with wafer with (backside or back or rear or opposite)	2
<input type="checkbox"/>	L28	L27 and wafer	126
<input type="checkbox"/>	L27	isotropic\$4 with conduct\$3 with film	687
<input type="checkbox"/>	L26	resin with particles with (gold or silver or copper)	4313

DB=USPT; PLUR=YES; OP=OR

<input type="checkbox"/>	L25	FC-262	1
<input type="checkbox"/>	L24	FC-262	1
<input type="checkbox"/>	L23	FC-262	1

DB=PGPB; PLUR=YES; OP=OR

<input type="checkbox"/>	L22	FC-262	1
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DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

<input type="checkbox"/>	L21	(isotropic\$4 with polymeric with film with conduct\$4)	7
<input type="checkbox"/>	L20	(isotropic\$4 with polymeric with film)	99
<input type="checkbox"/>	L19	(polymeric adj film) with backside with wafer	1
<input type="checkbox"/>	L18	(paste with wafer with backside)	21

DB=USPT; PLUR=YES; OP=OR

<input type="checkbox"/>	L17	paste with applied with wafer	110
<input type="checkbox"/>	L16	L14 and (chip with (back or backside))	17
<input type="checkbox"/>	L15	L14 and (chip with (back or backside))	17
<input type="checkbox"/>	L14	(conductive adj paste) with cur\$3	369

DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

<input type="checkbox"/>	L13	L12 and DRAM	13
<input type="checkbox"/>	L12	(conductive adj adhesive) with chip with (heatsink or sink)	218
<input type="checkbox"/>	L11	backside adj metallization	204
<input type="checkbox"/>	L10	L8 and backside	67
<input type="checkbox"/>	L9	L8 and (secur\$3 with backside)	3
<input type="checkbox"/>	L8	DRAM and (bias\$3 with (well or tub or n-well or p-well))	1693
<input type="checkbox"/>	L7	DRAM and (bias with backside)	38
<input type="checkbox"/>	L6	DRAM and (bias with backside)	38
<input type="checkbox"/>	L5	L4 and DRAM	21
<input type="checkbox"/>	L4	(bias with distribut\$4 with (region or well))	588
<input type="checkbox"/>	L3	L2 and DRAM	5
<input type="checkbox"/>	L2	L1 and (bias with front with back)	1485
<input type="checkbox"/>	L1	bias\$3 with ((front and back) or ((front adj side) and (back adj side)))	6468